

**Amendments to the Specification:**

Please replace the paragraph beginning on page 14, line 6 with the following rewritten paragraph:

[0001] The power source line control circuit 15 supplies a power source line control signal SCF for controlling the electrically conductive state of the control transistor Q through the power source line control line F. In addition, when the power source line control signal SCF switching the control transistor Q into an ON state is output from the power source line control circuit 15, the control transistor Q is switched into the ON state. As a result, the driving voltage Vdd is applied to the source of the adjusting transistor Trc.